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~~Panel Discussion and  
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~~2020 Programming  
Embedded Systems~~

~~(Vahid/Givargis):~~

~~Overview of the book  
and tools~~

~~Introduction to~~

~~Karnaugh Maps~~

~~Combinational Logic~~

~~Circuits, Functions,~~

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~~/u0026 Truth Tables~~

---

Logic Gates, Truth  
Tables, Boolean

Algebra - AND, OR,  
NOT, NAND /u0026

NOR Custom single  
purpose processor

design in embedded  
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chapter 2 Digital

Design: Sequential

Circuit Design Review

Lecture #1

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your computer -  
Bettina Bair System  
Design Interview  
Question: DESIGN A  
PARKING LOT asked  
at Google, Facebook  
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Design Preparation

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(SIP) System Design  
Interview - Approach  
and structure - How  
To [Part1] Parking Lot  
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Interview Question  
~~Digital Electronics:~~  
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custom digital  
processors. Describes  
various memory  
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Frank Vahid is the author of Digital Design with RTL Design, VHDL, and Verilog, 2nd Edition, published by Wiley.

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Digital Design

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Converting to

Boolean Equations •

Q1. A fire sprinkler system should spray water if high heat is sensed and the system is set to enabled. – Answer:  
Let Boolean variable  $h$  represent “ high heat is sensed, ”  $e$  represent

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“enabled,” and F represent “spraying water.” Then an equation is:  $F = h$  AND e.

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~~Combinational Logic~~  
~~Design~~  
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Designing increasingly complex digital design levels, starting with combinational logic, then sequential logic, datapath components, and finally register-transfer ...

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Create a High-Level  
State Machine •  
Let ' s consider each

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step of the RTL design process in more detail • Step 1

– Soda dispenser example – Not an FSM because: •

Multi-bit (data) inputs a and s •

Local register tot •

Data operations

$tot=0$ ,  $tot<s$ ,

$tot=tot+a$ . – Useful

high-level state

machine:

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